



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/987,409	11/14/2001	Kazunori Sakurai	111109	7050
25944	7590	05/17/2004		
OLIFF & BERRIDGE, PLC P.O. BOX 19928 ALEXANDRIA, VA 22320			EXAMINER KILDAY, LISA A	
			ART UNIT 2829	PAPER NUMBER

DATE MAILED: 05/17/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Applicati n No.

09/987,409

Applicant(s)

SAKURAI, KAZUNORI

Examin r

Lisa A Kilday

Art Unit

2829

PN

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on after final filed on 2/20/4 and 3/19/4.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 2,3,5,7,8 and 18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 2,3,5,7,8 and 18 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

Response to Amendment

The after final arguments filed on 2/20/04 and 3/19/04 have been considered. Examiner believes that the rejection of Tsukahara is correct and anticipates claims 1-7, 17-18. Nevertheless, the examiner is submitting a final rejection with identical reference numbers and a further explanation of the rejection to clarify the previous office action. Thus, the after final amendment of 3/19/04 is entered and due to this amendment, the repeated rejection under 102(b) over Tsukahara is made final.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 2-3, 5, 7-8, 18 are rejected under 35 U.S.C. 102(b) as being anticipated by Tsukahara (6,051,093).

In re claim 18, Tsukahara discloses in figs. 14a-d a method of manufacturing a semiconductor device including a step of mounting a semiconductor chip on a wiring substrate having a base substrate (see fig. 14c where the base substrate 21 is part of the wiring substrate which comprises references 4 + 21) on which are formed interconnecting lines (see ref. 33 in fig. 14b where ref. 33 is part of the interconnecting lines found on the wiring substrate), the method comprising: mounting the semiconductor chip (1) on the base substrate (the area on top near the chip) so that a surface of the base substrate opposite to the surface on which the interconnecting lines

(the lines are formed on the bottom of ref. 21 at the interface with ref. 4) are formed is in contact with bumps (see ref. 15 in fig. 14b where the bumps are down and touch the surface and in fig. 14c where the bumps penetrate the substrate and contact is with the base substrate) formed on the semiconductor chip (1); and electrically connecting the bumps to the interconnecting lines by heating at least the bumps and pressing the semiconductor chip (col. 8, lines 58-60; fig. 14c) toward the base substrate so that the base substrate is melted and the bumps are pushed into the base substrate (abstract; col. 8, lines 40-65).

In re claim 2, Tsukahara discloses the method of manufacturing a semiconductor device, wherein the interconnecting lines comprise connecting portions electrically connecting with the bumps (shown on top of the substrate (4) in figure 8(d)), and wherein in the step of electrical connection, the base substrate is melted, so that connections of the bumps and the connecting portions are sealed with the melted material of the base substrate (col. 8, lines 55-60).

In re claim 3, Tsukahara discloses that the melted material of the base substrate is adhered closely to a surface of the semiconductor chip (col. 8, lines 55-66).

In re claim 5, Tsukahara discloses wherein a thermoplastic resin is used as the base substrate (col. 8, lines 43-45).

In re claim 7, Tsukahara discloses further comprising: mounting another semiconductor chip on the wiring substrate (fig. 11; col. 8, lines 1-16).

Double Patenting

Claims of the instant application may be subject to a provisional rejection under the judicially created doctrine of obviousness-type double patenting as being unpatentable copending Application No. 09/794,666 depending on the final forms of the claims in both applications. The instant claims are not subject to double patenting, but may be subject to provisional double patenting depending on the final form of the claims.

Conclusion

Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry of a general nature or relating to the status of this application should be directed to the Group Receptionist whose telephone number is (703) 308-0957. See MPEP 203.08.

Any inquiry concerning this communication from the examiner should be directed to Lisa Kilday whose telephone number is (703) 306-5728. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kamand Cuneo,

Application/Control Number: 09/987,409

Page 5


Art Unit: 2829

can be reached on (703) 308-1233. The fax number for the group is (703) 305-3432. MPEP 502.01 contains instructions regarding procedures used in submitting responses by facsimile transmission.

Lisa Kilday

LAK

5/5/04


KAMAND CUNEO
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800